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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Title: MULTIMEDIA INTERFACE HAVING A PROCESSOR AND RECONFIGURABLE LOGIC (As Previously Amended)

Serial No.: 09/929,515

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Applicant(s): MUTHUKUMARASWAMY, et al.

Examiner: Cao, C..

Art Unit: 2185

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**Technology Center 2100**

**AMENDMENT**

Please enter the following.

Amendments to the Claims

Please cancel claims 49, 50, 51, 55, 56, and 57.

Please amend claims 29, 30, 31, 32, 33, 38, 39, 40, 41, 42, 52 and 58 as follows.

Please enter new claim 59.

A complete set of claims, with (status) indicated, is provided herewith.

23. (Pending) Multimedia interface, comprising:

an integrated circuit (IC) chip;

a block of reconfigurable logic incorporated on the IC chip; and

a block of media processor with a virtual instruction set capable of implementing a variety of multimedia algorithms incorporated on the IC chip separately from the reconfigurable logic block.

24. (Pending) The multimedia interface, according to claim 23, further comprising audio and/or video CODEC incorporated on the IC chip.

25. (Pending) The multimedia interface, according to claim 23, further comprising a phase locked loop (PLL) circuitry incorporated on the IC chip.